

FIG. 1

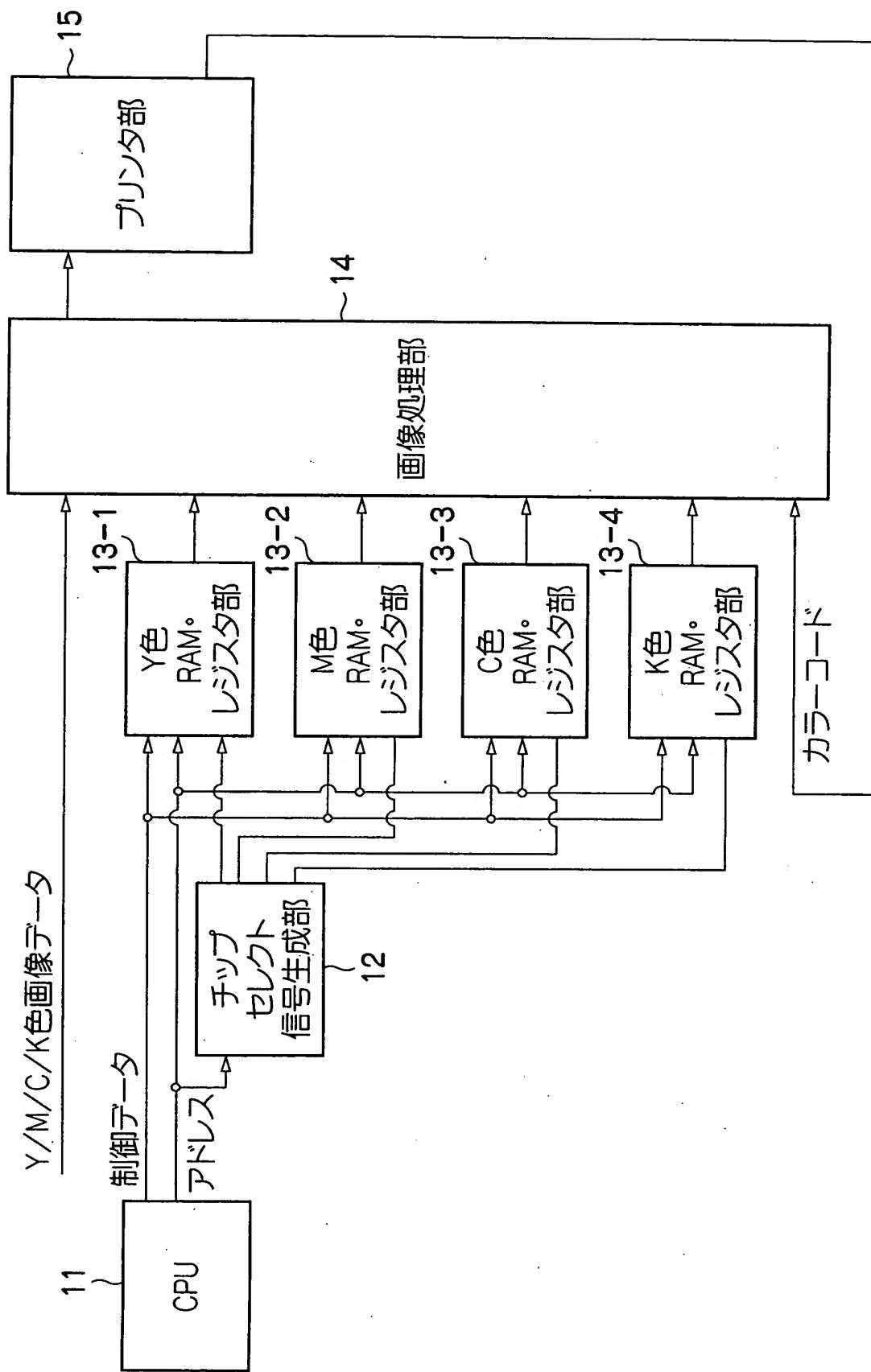
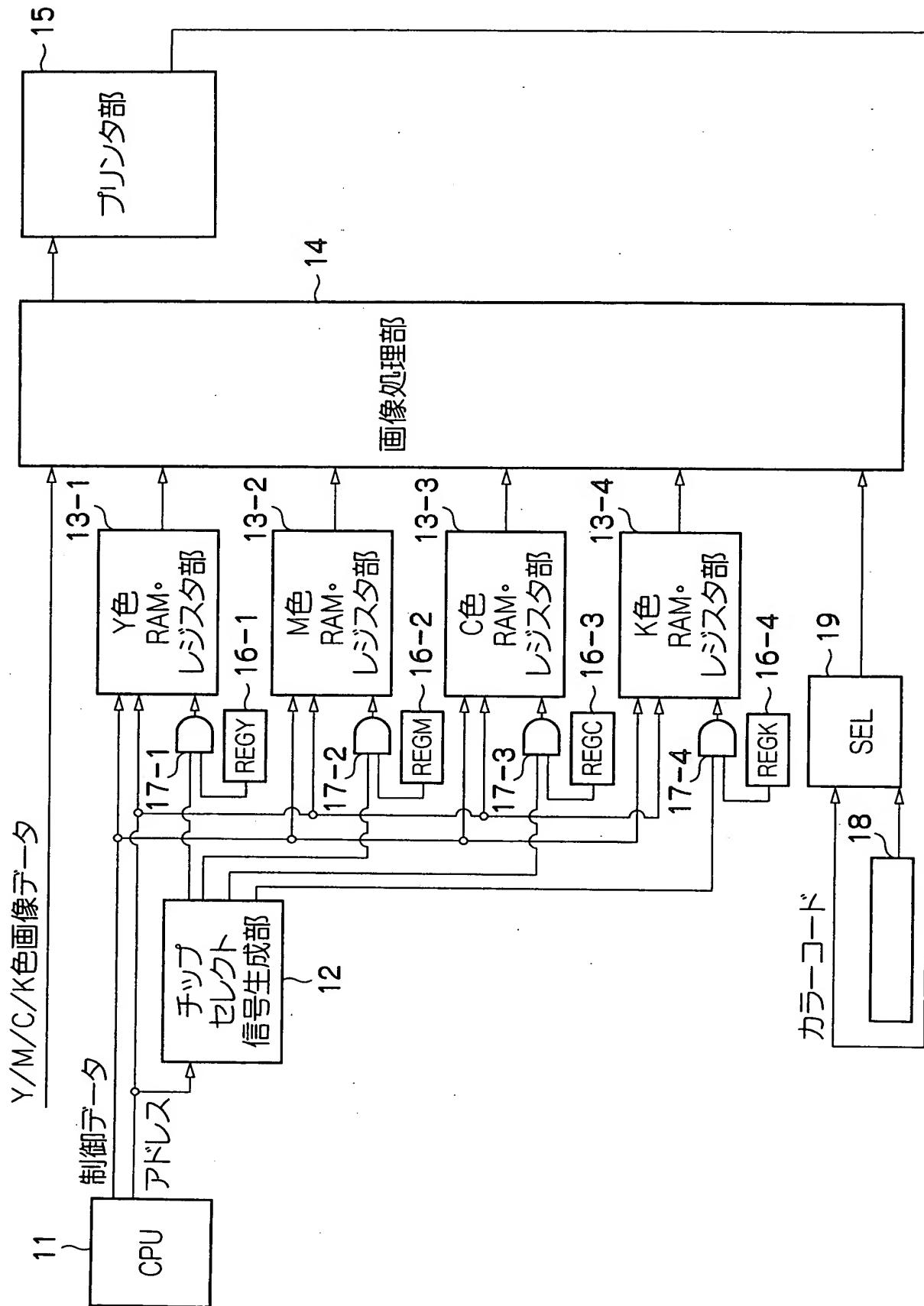


FIG.2

CPUアドレス	チップセレクト信号	対象RAM・レジスタ部	RAM・レジスタ部 内アドレス
0000h~0FFFh	YCS-0	Y色RAM・レジスタ部	000h-FFFFh
1000h~1FFFh	MCS-0	M色RAM・レジスタ部	000h-FFFFh
2000h~2FFFh	CCS-0	C色RAM・レジスタ部	000h-FFFFh
3000h~3FFFh	KCS-0	K色RAM・レジスタ部	000h-FFFFh

FIG.3



Title: COLOR IMAGE FORMING  
APPARATUS  
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## FIG.4

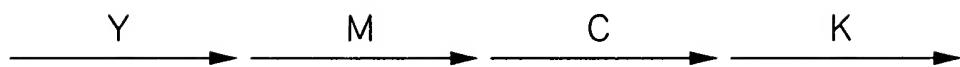


FIG.5

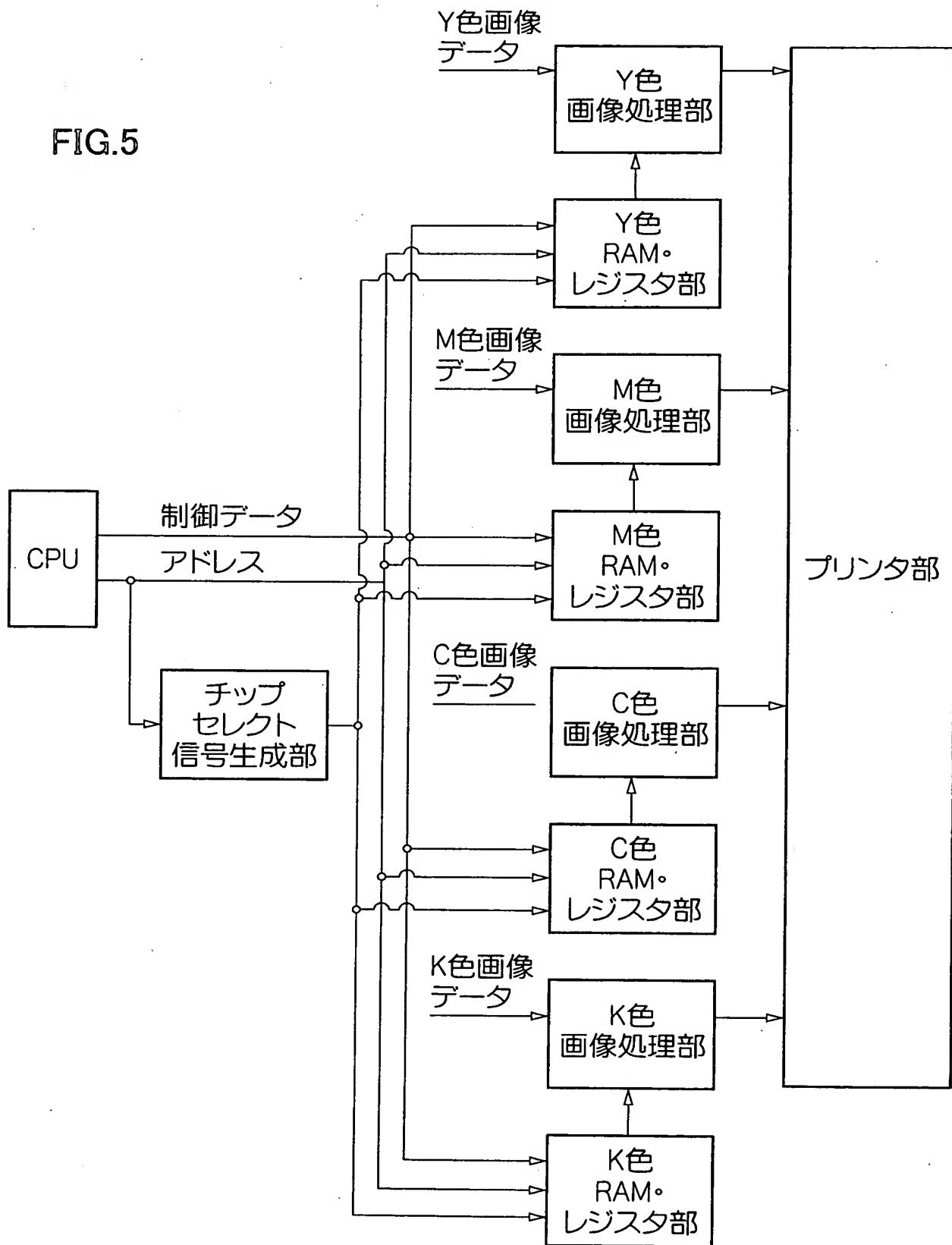


FIG.6

